

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	126	(380/228).CCLS.	USPAT; USOCR	OR	OFF	2006/05/20 20:33
L2	176	(709/209).CCLS.	USPAT; USOCR	OR	OFF	2006/05/20 20:33
L3	176	(709/209).CCLS.	USPAT; USOCR	OR	OFF	2006/05/20 20:33
L4	709	(709/221).CCLS.	USPAT; USOCR	OR	OFF	2006/05/20 20:33
L5	776	716/16	USPAT	OR	OFF	2006/05/20 20:33
L6	871	716/17	USPAT	OR	OFF	2006/05/20 20:33
L7	230	(hardware adj configuration) same (encrypt or encrypted)	USPAT	OR	OFF	2006/05/20 21:20
L8	30	(hardware adj configuration) same (encrypt or encrypted)	USPAT	OR	OFF	2006/05/20 21:20
L9	619	(hardware adj configuration) and (programmable adj4 (gate or logic or array))	USPAT	OR	OFF	2006/05/20 21:20
L10	597	(hardware adj configuration) and (programmable adj (gate or logic or array))	USPAT	OR	OFF	2006/05/20 21:20
L11	87	((hardware adj configuration) and (programmable adj (gate or logic or array))) and (encryption or encrypt or encrypted)	USPAT	OR	OFF	2006/05/20 21:20
L12	20	(hardware adj configuration).ti.	USPAT	OR	OFF	2006/05/20 21:20
L13	89	((Programmable adj array) or ((Programmable adj array) near2 logic)) and public) and private	USPAT	OR	OFF	2006/05/20 21:20
L14	3	BIOS adj key	USPAT	OR	OFF	2006/05/20 21:20
L15	2310	regular adj interval	USPAT	OR	OFF	2006/05/20 21:21
L16	0	8 and 15	USPAT	OR	OFF	2006/05/20 21:21
L17	0	9 and 15	USPAT	OR	OFF	2006/05/20 21:21
L18	9022	hardware adj configuration	USPAT	OR	OFF	2006/05/20 21:21
L19	21	15 and 18	USPAT	OR	OFF	2006/05/20 21:30
L20	26	update same (regular adj interval)	USPAT	OR	OFF	2006/05/20 21:30
L21	93	hardware adj reconfiguration	USPAT	OR	OFF	2006/05/20 22:09
L22	708	9 or 21	USPAT	OR	OFF	2006/05/20 21:30
L23	1	22 and (regular with update)	USPAT	OR	ON	2006/05/20 21:31
L24	6	22 and (periodic with update)	USPAT	OR	ON	2006/05/20 22:05

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L25	1108	(713/100).CCLS.	US-PGPUB; USPAT; EPO	OR	OFF	2006/05/20 22:04
L26	145	25 and (periodic or regular)	USPAT	OR	ON	2006/05/20 22:06
S1	6	"5530749"	USPAT	OR	OFF	2004/07/22 11:02
S2	4028	decrypt and encrypt	USPAT	OR	ON	2004/07/22 11:02
S3	202	S2 and (hardware adj configuration)	USPAT	OR	ON	2004/07/22 11:02
S4	8712	hardware adj configuration	USPAT	OR	ON	2004/07/22 11:12
S5	31	S4 same (decryption or decrypt or encryption or encrypt)	USPAT	OR	ON	2004/07/22 11:05
S6	10	S4 same (decryption or decrypt)	USPAT	OR	ON	2004/07/22 11:14
S7	11	(hardware adj configuration).ti.	USPAT	OR	ON	2004/07/22 11:12
S8	404	(hardware adj configuration)and (decryption or decrypt)	USPAT	OR	ON	2004/07/22 11:14
S9	404	(hardware adj configuration) and (decryption or decrypt)	USPAT	OR	ON	2004/07/22 11:15
S10	2	(hardware adj configuration) and (decryption or decrypt).ti.	USPAT	OR	ON	2004/07/22 11:51
S11	80	(decryption or decrypts).ti.	USPAT	OR	ON	2004/07/22 12:35
S12	6	"5,530,749"	USPAT	OR	ON	2004/07/22 12:39
S13	79	decryption.ti.	USPAT	OR	ON	2004/07/22 12:44
S14	84	decryption.ti.SSL.ti.	USPAT	OR	ON	2004/07/22 12:44
S15	5	SSL.ti.	USPAT	OR	ON	2004/07/22 12:44
S16	667	secure adj sockets adj layer	USPAT	OR	ON	2004/07/22 12:45
S17	2	(secure adj sockets adj layer).ti.	USPAT	OR	ON	2004/07/22 12:45
S18	4	IHAD	USPAT	OR	OFF	2005/04/28 19:57
S19	7237	hardware adj configuration	USPAT	OR	OFF	2004/07/14 14:16
S20	22	(hardware adj configuration) same (encrypt or encrypted)	USPAT	OR	OFF	2004/07/14 14:12
S21	9	((hardware adj configuration) same (encrypt or encrypted)) and (programmable same (gate or logic or array))	USPAT	OR	OFF	2004/07/14 14:14
S22	464	(hardware adj configuration) and (programmable adj4 (gate or logic or array))	USPAT	OR	OFF	2004/07/14 14:15
S23	445	(hardware adj configuration) and (programmable adj (gate or logic or array))	USPAT	OR	OFF	2004/07/14 14:15

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S24	61	((hardware adj configuration) and (programmable adj (gate or logic or array))) and (encryption or encrypt or encrypted)	USPAT	OR	OFF	2004/07/14 14:15
S25	11	(hardware adj configuration).ti.	USPAT	OR	OFF	2004/07/14 14:16
S26	11	(hardware adj configuration).ti.	USPAT	OR	ON	2004/07/14 14:17
S27	112349	CMOS or BIOS	USPAT	OR	ON	2004/07/14 14:18
S28	2	encrypted adj cmos	USPAT	OR	ON	2004/07/14 14:18
S29	5	encrypted adj bios	USPAT	OR	ON	2004/07/16 14:27
S30	92	(380/228).CCLS.	USPAT; USOCR	OR	OFF	2004/07/16 14:28
S31	193	(380/59).CCLS.	USPAT; USOCR	OR	OFF	2004/07/16 14:29
S32	134	(709/209).CCLS.	USPAT; USOCR	OR	OFF	2004/07/16 14:29
S33	508	(709/221).CCLS.	USPAT; USOCR	OR	OFF	2004/07/16 14:30
S34	3864	Programmable adj logic adj array	USPAT	OR	OFF	2004/07/16 14:30
S35	6285	PLA	USPAT	OR	OFF	2004/07/16 14:30
S36	15709	PAL	USPAT	OR	OFF	2004/07/16 14:30
S37	2665	Programmable adj array	USPAT	OR	OFF	2004/07/16 14:30
S38	2259	(Programmable adj array) near2 logic	USPAT	OR	OFF	2004/07/16 14:32
S39	605	716/16	USPAT	OR	OFF	2004/07/16 14:32
S40	686	716/17	USPAT	OR	OFF	2004/07/16 14:32
S41	33	716/17 and public	USPAT	OR	OFF	2004/07/16 14:32
S42	32	716/16 and public	USPAT	OR	OFF	2004/07/16 14:32
S43	2665	(Programmable adj array) or ((Programmable adj array) near2 logic) and public	USPAT	OR	OFF	2004/07/16 14:32
S44	199	((Programmable adj array) or ((Programmable adj array) near2 logic)) and public	USPAT	OR	OFF	2004/07/16 14:32
S45	76	(((Programmable adj array) or ((Programmable adj array) near2 logic)) and public) and private	USPAT	OR	OFF	2004/07/16 14:32
S46	10	(Programmable adj logic adj array) and cryptographic	USPAT	OR	OFF	2004/07/16 14:32
S47	0	(Programmable adj logic adj array) and cryptography	USPAT	OR	OFF	2004/07/16 14:33
S48	0	(Programmable adj logic adj array) and cryptographhy	USPAT	OR	OFF	2004/07/16 14:33

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S49	2665	(Programmable adj array) or ((Programmable adj array) near2 logic)	USPAT	OR	OFF	2004/07/16 14:33
S50	168	((Programmable adj array) or ((Programmable adj array) near2 logic)) and bios	USPAT	OR	OFF	2004/07/16 14:33
S51	678	((Programmable adj array) or ((Programmable adj array) near2 logic)) and CMOS	USPAT	OR	OFF	2004/07/16 14:33
S52	743	(((Programmable adj array) or ((Programmable adj array) near2 logic)) and bios) or (((Programmable adj array) or ((Programmable adj array) near2 logic)) and CMOS)	USPAT	OR	OFF	2004/07/16 14:33
S53	3	(((Programmable adj array) or ((Programmable adj array) near2 logic)) and bios) or (((Programmable adj array) or ((Programmable adj array) near2 logic)) and CMOS)) and (encrypt or decrypt)	USPAT	OR	ON	2004/07/16 14:33
S54	1	"6810514"	USPAT	OR	OFF	2005/04/28 19:58
S55	50	"5535406"	USPAT	OR	OFF	2005/04/28 19:58
S56	6	S55 and key	USPAT	OR	OFF	2005/04/28 19:59
S57	1	S56 and (encrypt or encryption or encrypted)	USPAT	OR	ON	2005/04/28 20:00
S58	51	"5705938"	USPAT	OR	ON	2005/04/28 20:00
S59	13667	key and (encrypt or encryption or encrypted)	USPAT	OR	ON	2005/04/28 20:00
S60	10	S58 and S59	USPAT	OR	ON	2005/04/28 20:01
S61	2489	xilinx	USPAT	OR	ON	2005/04/28 20:01
S62	19	"6020758"	USPAT	OR	ON	2005/04/28 20:01
S63	19	S62 and S62	USPAT	OR	ON	2005/04/28 20:01
S64	2	S62 and S59	USPAT	OR	ON	2005/04/28 20:02
S65	12	"6191614"	USPAT	OR	ON	2005/04/28 20:02
S66	1	S65 and S59	USPAT	OR	ON	2005/04/28 20:02
S67	118	(380/228).CCLS.	USPAT; USOCR	OR	OFF	2005/04/28 21:32
S68	246	(380/59).CCLS.	USPAT; USOCR	OR	OFF	2005/04/28 21:32
S69	146	(709/209).CCLS.	USPAT; USOCR	OR	OFF	2005/04/28 21:32
S70	577	(709/221).CCLS.	USPAT; USOCR	OR	OFF	2005/04/28 21:32
S71	569	716/16	USPAT	OR	OFF	2005/04/28 21:32

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S72	657	716/17	USPAT	OR	OFF	2005/04/28 21:40
S73	155	S70 and key	USPAT	OR	OFF	2005/04/28 21:40
S74	88	S71 and key	USPAT	OR	OFF	2005/04/28 21:40
S75	111	S72 and key	USPAT	OR	OFF	2005/04/28 21:40
S76	65	S67 and key	USPAT	OR	OFF	2005/04/28 21:40
S77	97	S68 and key	USPAT	OR	OFF	2005/04/28 21:40
S78	36	S69 and key	USPAT	OR	OFF	2005/04/28 21:40
S79	3	BIOS adj key	USPAT	OR	OFF	2005/04/28 21:40
S80	37	CMOS adj key	USPAT	OR	OFF	2005/04/28 21:40
S81	37	(hardware adj configuration) with key	USPAT	OR	OFF	2005/04/28 21:41
S82	123	(380/228).CCLS.	USPAT; USOCR	OR	OFF	2005/12/06 19:16
S83	250	(380/59).CCLS.	USPAT; USOCR	OR	OFF	2005/12/06 19:16
S84	161	(709/209).CCLS.	USPAT; USOCR	OR	OFF	2005/12/06 19:20
S85	649	(709/221).CCLS.	USPAT; USOCR	OR	OFF	2005/12/06 19:20
S86	679	716/16	USPAT	OR	OFF	2005/12/06 19:20
S87	758	716/17	USPAT	OR	OFF	2005/12/06 19:20
S88	29	(hardware adj configuration) same (encrypt or encrypted)	USPAT	OR	OFF	2005/12/06 19:20
S89	11	((hardware adj configuration) same (encrypt or encrypted)) and (programmable same (gate or logic or array))	USPAT	OR	OFF	2005/12/06 19:20
S90	74	((hardware adj configuration) and (programmable adj (gate or logic or array))) and (encryption or encrypt or encrypted)	USPAT	OR	OFF	2005/12/06 19:20
S91	17	(hardware adj configuration).ti.	USPAT	OR	OFF	2005/12/06 19:20
S92	18	(hardware adj configuration).ti.	USPAT	OR	ON	2005/12/06 19:20
S93	4160	Programmable adj logic adj array	USPAT	OR	OFF	2005/12/06 19:20
S94	2388	(Programmable adj array) near2 logic	USPAT	OR	OFF	2005/12/06 19:21
S95	6061	S93 OR S94	USPAT	OR	OFF	2005/12/06 19:21
S96	1255	S95 and key	USPAT	OR	OFF	2005/12/06 19:21
S97	806	S96 and configuration	USPAT	OR	OFF	2005/12/06 19:21
S98	7	S97 and cryptographic	USPAT	OR	OFF	2005/12/06 19:21